COMBINED TECHNICAL SERVICES EXAMINATION

(NON INTERVIEW POST)

COMPUTER BASED TEST

DATE OF EXAM: 09.08.2025 FN

PAPER - II

COMPUTER SCIENCE, INFORMATION
TECHNOLOGY, ELECTRICAL, ELECTRONICS
AND COMMUNICATION ENGINEERING
(DEGREE STANDARD) (CODE:554)

QNO-77 QNO 66, 100, 184, 186,

- 1. The magnetic field strength of solenoid can be increased by
 - (A) Decreasing the current
 - (B) Decreasing the number of turns
 - (C) Removing iron from the field
 - (D) Increasing the current
 - (E) Answer not known
- 2. LVDT is a
 - (A) Passive Capacitive Transducer
 - ✓(B) Passive Inductive Transducer
 - (C) Active Capacitive Transducer
 - (D) Active Inductive Transducers
 - (E) Answer not known
- 3. Which photo electric device is most suitable for digital applications?
 - (A) Photo-emissive cell
- (B) Photo diode

- (C) Photo transistor
- (D) Photo voltaic cell
- (E) Answer not known
- 4. A Linear Variable Differential Transformer (LVDT) is
 - ✓(A) a displacement transducer
 - (B) an impedance matching transformer
 - (C) a differential temperature sensor
 - (D) an auto transformer
 - (E) Answer not known
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5. In a 3 phase symmetrical and balanced system, the sum of instantaneous values of emfs of three phase is

✓ (A) always zero

(B) $e_R + e_Y + e_B$

(C) $e_R - e_Y + e_B$

(D) $e_R - e_Y - e_B$

(E) Answer not known

6. In a balanced delta connected system the line current and phase current are related as

 $\checkmark (A) \quad I_L = \sqrt{3} \ I_{ph}$

(B) $I_L = I_{ph}$

(C) $I_{ph} = \sqrt{3} I_L$

(D) None of the above

(E) Answer not known

7. Energy stored in a capacitor is given by all of the following relations except

(i) E = QV/2

(ii) $E = CV^2/2$

(iii) $E = Q^2/2C$

(iv) E = QCV/2

✓ (A) (iv)

(B) (i) and (ii)

(C) (i) and (iii)

(D) All the four expression

(E) Answer not known

- 8. The basic step of a 9 bit DAC is 10.3 mV. If 000000000 represents 0V, what input is produced if the input is 101101111.
 - (A) 1.24 V

(B) 3.78 V

(C) 4.26 V

(D) 1.02 V

- (E) Answer not known
- 9. The ratio of transformation in potential transformers
 - (A) Increases with increase in power factor of secondary burden
 - ✓ (B) Decreases with increase in power factor of secondary burden
 - (C) Remains constant irrespective of power factor of secondary burden
 - (D) Proportional to square of the power factor of secondary burden
 - (E) Answer not known
- 10. During measurement of voltage and current in a load, ammeter and voltmeter are connected in series and across the load respectively. If voltmeter and ammeter positions are interchanged by mistake, then
 - (A) Voltmeter will be damaged
 - (B) Ammeter will be damaged
 - (C) Both Ammeter and Voltmeter will be damaged
 - (D) Both the meters are safe
 - (E) Answer not known

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[Turn over

11.	Nun coil	moving coil instrument aber of turns = 100, width of = 30 mm, Flux density in the ue = 30×10^{-6} Nm. The current	the he g	coil = 20 mm, Depth of the gap = 0.1 wb/m ² , deflection
	(A)	5 A	(B)	50 mA
~	(C)	5 mA	(D)	10 mA
	(E)	Answer not known		
12.		phase, 400 V, 50 Hz, 4 pole in rcent. Calculate the speed of th		
~	(A)	1425 rpm	(B)	zero
	(C)	75 rpm	(D)	1500 rpm
	(E)	Answer not known		
13.		iron loss of a certain transform at half load will be	er a	t full load is 100 watts. This
	(A)	50 w	(B)	25 w
	• •			100 w
		Answer not known	(-)	
14.		DC motor, speed control by tance provides a	cha	inging the armature circuit
	(A)	Constant power drive	(B)	Constant torque drive
V	(C)	Variable torque drive	(D)	Variable power drive
	(E)	Answer not known		
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15.	5. The limit of voltage harmonic distortion for any typical application is								
	(A)	3% ✓ (B) 5%							
	(C)	10% (D) 15%							
	(E)	Answer not known							
16.	In a	3-phase full converter, the six SCRs are fired at an interval of							
	(A)	30° ✓ (B) 60°							
	(C)	90° (D) 120°							
	(E)	Answer not known							
17.	A PWM switching scheme is used in single phase inverters to								
	(A)	Reduce the total harmonic distortion							
	(B)	Minimise the load on the dc side							
	(C)	Increase the life of batteries							
~	(D)	Reduce low order harmonics and increase higher order harmonics							
	(E)	Answer not known .							
18.	Zene	er diode is operated in region.							
	(A)	Active							
~	(B)	Breakdown							
	(C)	Forward bias							
	(D)	Cut off							
	(E)	Answer not known							

19. In JFET, the channel conductivity is primarily controlled by

- (A) Drain to source voltage
- (B) Source to drain voltage
- √(C) Gate to source voltage
 - (D) Base to emitter voltage
 - (E) Answer not known

20. A JFET

- (A) is a current controlled device
- (B) has low input impedance
- ✓(C) is a voltage controlled device
 - (D) has very large voltage gain
 - (E) Answer not known

21. What does MAODV stand for?

- (A) Multicast Adhoc on-Demand distance vector
 - (B) Mobile Adhoc optimized Distance vector
 - (C) Multicast Adaptive on-Demand vector
 - (D) Modified Adhoc on-Demand vector
 - (E) Answer not known

22.	A pr	oactive routing protocol is also known as a routing ocol.
~	(A)	Table driven
	(B)	On-demand
	(C)	Hybrid
	(D)	All the above
	(E)	Answer not known
23.		node wants to know the current location of a mobile Node (MN), nds a request to Home Agent (HA). The method is called
	(A)	Binding update
	(B)	Binding warning
~	(C)	Binding request
	(D)	Binding acknowledgement
	(E)	Answer not known
24.	The (MN	defines the current location of the mobile node from an IP point of view.
	(A)	Foreign Agent (FA)
~	(B)	Care of address (CoA)
	(C)	Home Network
	(D)	Foreign Network
	(E)	Answer not known

- 25. For any given code h, it is computationally infeasible to find x such that H(x) = h. A hash function with this property is referred to as
 - (A) Second preimage resistant
 - ✓ (B) One way or preimage resistant
 - (C) Collision resistant
 - (D) Strong Collision resistant
 - (E) Answer not known
- 26. _____ is defined as an attack in which the adversary chooses a number of cipher texts and is then given the corresponding plaintexts, decrypted with the target's private key.
 - (A) Ciphertext attack
 - ✓ (B) Chosen ciphertext attack
 - (C) Malware attack
 - (D) Timing attack
 - (E) Answer not known
- 27. Vigenere cipher belongs to
 - (A) Poly alphabetic ciphers
 - (B) Mono alphabetic ciphers
 - (C) Bi alphabetic ciphers
 - (D) Multi alphabetic ciphers
 - (E) Answer not known

28.	The mon	algorithm which defines the details of how to itor the Queue length and when to drop a packet.										
~	(A)	A) RED – Random Early Detection										
	(B)	DEC bit										
	(C)	Source based congestion										
	(D)	Leaky bucket algorithm										
	(E)	Answer not known										
29.		The mechanism is used to convert domain name into IP address is known as										
	(A)	URL										
~	(B)	DNS										
	(C)	FTP										
	(D)	HTTP										
	(E)	Answer not known										
30.	Whi	ch of the following WLAN standard has been named WiFi?										
	(A)	IEEE 802.6										
	(B)	IEEE 802.15.4										
~	(C)	DSSS IEEE 802.11b										
	(D)	IEEE 802.11g										
	(E)	Answer not known										

31.	the	is designed to improve network efficiency by reducing number of packets sent particularly for streams of small data.
	(A)	Dynamic routing algorithm
~	(B)	Nagle's algorithm
	(C)	Link-state Routing algorithm
	(D)	Dijkstra's shortest path algorithm
	(E)	Answer not known
32.	A sir	mple protocol used for fetching email from a remote mail box is
~	(A)	POP3
	(B)	IMAP
	(C)	DMSP
	(D)	SMTP
	(E)	Answer not known
33.	User	Datagram Protocol (UDP) is called connectionless because
~	(A)	All UDP packets are treated independently by transport layer
	(B)	It sends data as a stream of related packets
	(C)	It is received in the same order as sent order
	(D)	It sends data very quickly
	(E)	Answer not known

34.	technology is one of the most promising for supporting high speed digital communication over the existing telephone.						
	(A)	CMTS					
	(B)	DSL					
	(C)	MODEM					
	(D)	Cable					
	(E)	Answer not known					
35.		is a non-adaptive routing algorithm where every ming data packet is forwarded to all outgoing links except the it arrived on					
~	(A)	Flooding					
	(B)	Distance vector routing					
	(C)	Path vector algm.					
	(D)	Link state routing					
	(E)	Answer not known					
36.	rout	is an IP address allocation method that improves data ing efficiency on the internet.					
	(A)	ICMP					
	(B)	DHCP					
	(C)	ARP					
~	(D)	CIDR					
	(E)	Answer not known					

- 37. The three layers in Asynchronous Transfer Mode (ATM) are
 - ✓ (A) ATM Adaptation Layer (AAL), ATM Layer and Physical Layer
 - (B) Application Layer, Transport Layer and ATM
 - (C) Session Layer, ATM Layer and Physical Layer
 - (D) Internet Layer, Application Layer and ATM Layer
 - (E) Answer not known
- 38. What is the main purpose of using collision free protocols in computer networks?
 - (A) To reduce network traffic
 - (B) To improve network security
 - ✓(C) To increase network efficiency and reduce contention
 - (D) To decrease latency
 - (E) Answer not known
- 39. Which of the following protocol is responsible for converting higher level protocol address to physical network address?
 - (A) Reverse Address Resolution Protocol (RARP)
 - (B) Bootstrap Protocol (BOOTP)
 - ✓(C) Address Resolution Protocol (ARP)
 - (D) Internet Control Message Protocol (ICMP)
 - (E) Answer not known

- 40. The technique of temporarily delaying outgoing acknowledgements so that they can be hooked on to the next outgoing data frame is called
 - (A) Cyclic redundancy check
 - (B) Parity check
 - ✓ (C) Piggybacking
 - (D) Fletcher's checksum
 - (E) Answer not known
- 41. In linked allocation
 - (A) Each file must occupy a set of contiguous blocks of the disk
 - (B) Each file is a linked list of storage blocks
 - (C) All the pointers are scattered blocks are placed together in one location
 - (D) None of these
 - (E) Answer not known
- 42. Which among the following is not an essential characteristic of NIST cloud model?
 - (A) Rapid elasticity
- (B) Multi-tenancy
- (C) Resource pooling

- (D) Broad network access
- (E) Answer not known

43.		Which among the essential characteristics of cloud computing is not efficient in grid computing model?								
~	(A)	Rapid elasticity	(B)	Resource sharing						
	(C)	Distributed service	(D)	Both (A) and (B)						
	(E)	Answer not known								
44.	File	type can be represented by								
	(A)	File name	(B)	File extension						
	(C)	File identifier	(D)	Binary						
	(E)	Answer not known								
45.	The	software layer implementing v	virtua	alization is known as						
	(A)	Operating systems	(B)	Hypervisor						
	(C)	Application layer	(D)	None of these						
	(E)	Answer not known								
46.	Disk	address consists of								
	(A)	Cylinder number, track num	ber							
	(B)	Cylinder number, track num	ber, s	sector number						
	(C)	Cylinder number, sector num	ıber							
	(D)	None of the above								
	(E)	Answer not known								

47. Consider the following set of processes, assumed to have arrived at time o in the order $P_1, P_2, ..., P_5$ with the length of the CPU bust given in milliseconds

Process	Bust Time	Priority
P	10	3
P_2	1	1
P_3	2	4
P_4	1	5
P_5	5	2

Using priority scheduling, the average waiting time is

- \checkmark (A) 8 2 milliseconds
- (B) 6-5 milliseconds

(C) 7 milliseconds

- (D) 7-5 milliseconds
- (E) Answer not known
- 48. The _____ facility enables a user thread to specify a function that is to be called when the user thread receives notification of a particular event.
 - (A) Synchronous procedure call
 - ✓(B) Asynchronous procedure call
 - (C) Default procedure call
 - (D) User defined procedure call
 - (E) Answer not known

49.	A thread library available for Solaris systems and adopted in early versions of Java, uses the many to one model								
~	(A)	Green threads	(B)	Write threads					
	(C)	Blue threads	(D)	User threads					
	(E)	Answer not known		placks!					
50.		establish and maintain commu lows uses	ınica	tion between two processes					
~	(A)	Connection ports and commun	nicat	ion ports					
	(B)	Local ports and communication	n po	orts					
	(C)	System ports and Local ports							
	(D)	Parallel ports							
	(E)	Answer not known							
51.		focuses on distributing cores and percore.		osets of the same data across ning the same operation on					
	(A)	Task parallelism	(B)	Data parallelism					
	(C)	Process parallelism	,	None of the above					
	(E)	Answer not known							

52.	On 1	Unix	systems,	ordinary	pipes	are	constructed	using	the	function
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- 1. Pipe (fd [])
- 2. Pipe (int fd [])
- 3. Pipe (int)
- 4. Pipe ()
- (A) (1) only
- (B) (2) only
 - (C) (1) and (2) only
 - (D) (3) and (4) only
 - (E) Answer not known

53. When a process creates a new process, two possibilities for execution exist?

- 1. The parent continues to execute concurrently with its children
- 2. The parent waits until some or all of its children have terminated.
- 3. The children process is a duplicate of the parent process
- 4. The child process has a new program loaded into it
- (A) 1 and 2 are correct
- (B) 1 and 3 are correct
- (C) 2 and 4 are correct
- (D) 2 and 3 are correct
- (E) Answer not known

54.	is used when the source CPU type is different from the target CPU type								
	(A) (C) (E)	Virtualization (B) Emulation Interpretation (D) None of the above Answer not known							
55.	Linu	x uses to implement CPU scheduling algorithms.							
	(A) (C) (E)	general tree binary search tree Answer not known (B) binary tree (D) balanced binary search tree							
56.	Which one of the following is NOT the type of Linked List?								
	(A) (C) (E)	singly linked list circularly linked list Answer not known (B) doubly linked list (D) dotted linked list							
57.	Iden	tify the correct :							
	of A	oose an integer A in file B resides on Magnetic disk. If the value is to be incremented, which of the following correctly migrates CPU for execution.							
	(A)	Hardware register \rightarrow Cache \rightarrow Main Memory \rightarrow Magnetic disk							
	(B)	Cache \rightarrow Main Memory \rightarrow Magnetic disk \rightarrow Hardware register							
~	(C)	Magnetic disk \rightarrow Main Memory \rightarrow Cache \rightarrow Hardware register							
	(D) (E)	Cache \rightarrow Magnetic disk \rightarrow Main Memory \rightarrow Hardware Answer not known							

58. Choose the right answer:

Traditional UNIX system structure

- (A) is layered
- (B) the kernel provides file system, CPU scheduling and memory management
- (C) the kernel is modularised
- \checkmark (D) both (A) and (B)
 - (E) Answer not known

59. The I/O subsystem consists of

- (1) A memory management component that includes buffering, caching and spooling
- (2) A general device-driver interface
- (3) Drivers for specific hardware devices
- (4) All the above
- (A) (1)

(B) Both (1) and (2)

(C) (4)

- (D) Both (1) and (3)
- (E) Answer not known

60.	Choose the right answer among type:											
		Which of the following statements are true about Multicore Multiprocessor?										
	(i)	They can be more efficient core.	than	mu]	ltiple	chips v	with single					
	(ii)		One chip with multiple cores uses significantly less power than multiple single – core chips.									
	(iii)	It is a multiple single – core of	hip.									
	(A)	(i) only	(B)	(B) (i) and (ii) only								
	(C)					ii) only						
	(E)	Answer not known										
61.	Which property means that the copies of the same information item at the successive memory levels must be consistent?											
	(A)	Round-trip time	(B)	Incl	usion							
V	(C)	Coherence	(D)	Loca	ality o	f refere	nce					
	(E)	Answer not known										
62.	The	USB Pen drives and SD cards	are r	nade	up	n	nemory.					
	(A)		-		ND Fla							
	(C)	PROM	` ′		ROM							
	(E)	Answer not known										

63.		Which systems typically involve distributed clusters of processors connected by buses or local area networks?						
	(A)) Uniform Memory Access (UMA)						
~	(B)	(B) Non Uniform Memory Access (NUMA)						
	(C)	C) Direct Memory Access (DMA)						
	(D)	None of the above						
	(E)	Answer not known						
64.	Shared writable data are and remains in the main memory							
	(A)	Cachable	(B)	Non-Cachable				
	(C)	Centralized Cachable	(D)	Snoopy Cache				
	(E)	Answer not known						
65.	In which mapping, each word of cache, can store two or more words of memory under the same Index address?							
	(A)	Mapping	(B)	Associative Mapping				
	(C)	Direct Mapping	(D)	Set-Associative Mapping				
	(F)	Answer not known						

- 66. Which metric removes the discrepancy between systems with different numbers of clocks per operation by measuring instructions per second rather than clocks per second?
 - (A) Latency
 - (B) Through put
 - (C) Relative performance
 - (D) Millions of Instruction Per Second (MIPS)
 - (E) Answer not known
- 67. Conversion of 0.8125(10) into its binary fraction
 - (A) 0.1101₂

(B) 0.0011₂

(C) 0.1010_2

- (D) 0.1110₂
- (E) Answer not known
- 68. Find the sum and output carry of a half adder for set of input bits 01
 - (A) sum = 1, o/p carry = 0
- (B) sum = 1, o/p carry = 1
- (C) sum = 0, o/p carry = 0
- (D) sum = 0, o/p carry = 1
- (E) Answer not known

Choose the address mode for the following statement: 69.

"It is used in conjunction with two byte instructions where the first byte is op code and the second byte is the operand.

- (A) Direct Addressing mode
- **(B)** Immediate Addressing mode
 - (C) Inherent Addressing mode
 - (D) Index Addressing mode
 - Answer not known (E)

70. Choose the correct statement about Cache Memory

- (A) process that copies frequently accessed instructions
 - (B) contains program and data Associated with them
 - (C) system specific low level code
 - (D) page replacement
 - (E)Answer not known

71. Choose correct answer for the following Boolean expression

$$(x+y)(\overline{x}\,\overline{y})$$

- (B) 1
- (D) y
- Answer not known

72.	Name the Address mode which is used by a class of Instructions known as branch Instructions					
~	(A)	Relative Addressing		(B) I	mmediate Addressing
	(C)	Indexed Addressing		(D) Iı	ndirect Addressing
	(E)	Answer not known				
73.	Cho	ose the gate name wh	ich ha	ving	the ti	ruth table
			A	В	X	
			0	0	1	
			0	1	0	
			1	0	0	
			1	1	1	
	(A)	XOR		(B) N	IOR
,	(C)	XNOR		(D) N	JAND
	(E)	Answer not known				
74.		ch processor have 32 age CPI is less than 1		gene	ral p	ourpose registers and the
V	(A)	RISC		(B) C	ISC
	(C)	Vector		(D) S	uper scalar
	(E)	Λ nswer not known				
75.	Whi	ch hazard is otherwise	e knov	vn as	bran	ch hazard?
	(A)	Data Hazard		(B) S	tructural Hazard
· ·	(C)	Control Hazard				one of the above
	(E)	Answer not known				
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76.		ch hazard occurs when the ible combinations of the instr Data Hazard Control Hazard Answer not known	ruction (B)		ously? Hazard
77.	imag proc (A) (C)	rtain computer system takes ge, and this time in reduce essor card is added to the sys 0.75 .25 Answer not known	ced to stem. V	100 ms w What is the sp 1.25	hen a graphics
78.	mem		ctions e same (B)	may be read	ling or writings le? Hazard
79.		rlapped register in RISC sters, global regist	T-0-1		
~	(A) (C) (E)	74, 10, 4 70, 10, 2 Answer not known	1282 1882	62, 8, 2 74, 8, 4	

80.	How	many ADD instructions in V	AX?				
	(A)	20	(B) 21				
	(C)	25	(D) 30				
	(E)	Answer not known					
81.	Whi	Which manipulators set the format flag f in output?					
	(A)	setprecision (int d)	(B) setfill (int c)				
	(C)	setiosflags (long f)	(D) setw (int w)				
	(E)	Answer not known					
82.	Identify the invalid statement for virtual function.						
	(i)	Virtual functions must be members of some class.					
	(ii)	They can be static and dynamic members.					
	(iii)	They are accessed by using object pointers.					
	(iv)	A virtual function can be a friend of another class					
	(A)	(i)	(B) (ii)				
	(C)	(iii)	(D) (iv)				
	(E)	Answer not known					

00.		in Standard Template Library (STL).					
	(A)	Nonmutating algorithms					
	(B)	Mutating algorithms					
	(C)	Search algorithms					
	(D)	Relational algorithms					
	(E)	Answer not known					
84.		case class, some functions has been defined empty, then the					
	(A)	empty functions (B) virtual functions					
	(C)	pure virtual functions (D) void functions					
	(E)	Answer not known					
85.	Dynamic binding is associated with						
	(1)	Polymorphism					
	(2)	Inheritance					
	(3)	Encapsulation					
	(4)	Abstraction					
~	(A)	(1) and (2)					
	(B)	(1) only					
	(C)	(2) and (3)					
	(D)	(4) only					
	(E)	Answer not known					

86.	The feature by which obj					ect communicate with one another by mation is called				
	(A)	Da	ta bind	ling		✓(B) Message passing				
	(C)	Dat	ta tran	sfer		(D) Data reading				
	(E)	Ans	swer n	ot kno	wn					
87.	Arg	umer	nt to a	сору со	onstru	ictor				
~	(A) must be const					(B) must not be const				
	(C)	mu	st be in	nteger		(D) must be static				
	(E)	Ans	swer n	ot knov	wn					
88.	Mat	tch th	e follo	wing ty	ype:					
	(a)	Friend function			1.	make a program run faster but speed diminishes as the function grows in size				
	(b)	Static member variable			2.	eliminate and redundant code and extend the use of existing class				
	(c)	Inline function			3.	can be declared either in the public or private part of a class				
	(d)) Inheritance		4.	visible only within the class					
		(a)	(b)	(c)	(d)					
	(A)	4	3	1	2					
	(B)	4	1	3	2					
V	(C)	3	4	1	2					
	(D)	4	3	2	1					
	(E)	Ans	wer no	t knov	vn					

- 89. The method used to close a file is
 - ✓(A) file.close()

(B) close.file()

(C) close()

- (D) file close()
- (E) Answer not known
- 90. Choose the correct option for the code snippet below.

with open ("file.txt", "rb") as file:

for line in file:

print (line)

- (A) the file is opened for read in binary format but the file is not closed after for loop is over
- (B) after the file is used in the for loop, it is automatically closed after the loop is over
 - (C) compiler error
 - (D) does not raise error but fails to execute
 - (E) Answer not known

91.	Iden	tify the statements which are not true about Tuples
	(1)	Tuples can be used as key for a dictionary
	(2)	Tuples are mutable
	(3)	Tuples can be converted into list
	(4)	Λ tuple can be defined inside another Tuple
	(A)	(1) and (2)
	(B)	(2) and (3)
	(C)	(3) and (4)
V	(D)	(2) only
	(E)	Answer not known
92.	Whi	ch operator is also known as string repetition operator?
	(A)	+ (B) *
	(C)	& (D) ^
	(E)	Answer not known
93.		variables of the same structure can be copied the same way as nary variables
~	(A)	Yes
	(B)	Compiler Error
	(C)	Syntax Error

Answer not known

(D)

(E)

Garbage values will be the resultant

94.	. The default C storage class for a variable					
	(A)	int				
	(B)	static				
~	(C)	auto				
	(D)	extern				
	(E)	Answer not known				
95.	The	major difference between structure and union is in the				
	(A)	Usage (B) Assigning members				
	(C)	Defining members (D) Storage				
	(E)	Answer not known				
96.	The	scope of local variable in C will be for				
	(A)	The entire program				
	(B)	The entire file				
~	(C)	The function in which it is declared				
	(D)	All the functions in the program				
	(E)	Answer not known				

97.	The following statement is equivalent in C programming statement
	listed below
	Cl

Char * name [3] = {"New",

"Australia",

"India"}

(A) Char name [0] [25];

(B) Char name [3];

(C) Char name [4];

√(D) Char name [3] [25];

(E) Answer not known

98. Pick out the formatted I/O function in C language

(A) printf()

(B) getchar()

(C) putch()

(D) gets()

(E) Answer not known

99. In the C program the equality operator is represented by

(A) :=

(B) · EQ ·

(C) =

✓(D) ==

(E) Answer not known

100. The expression for (;exp2;) is a valid expression in C program justify

(A) The loop will not terminate - Invalid statement in C

(B) Syntax error - Invalid statement in C

✓(C) Valid statement in C

(D) None of the above

(E) Answer not known

101.	indej durir	is a circuit for emulating pendent of a particular tanged the development phase for incorporate a particular micro	gete mos	d system processor, usable st of the target systems that
	(A)	Compiler	, ,	Emulator
	(C)	Debugger	(D)	In-Circuit Emulator (ICE)
	(E)	Answer not known		
102.		e combinations of events from the wrong order, may cre		ne two tasks operate on the
~	(A)	Race condition that causes en	rone	ous operation
	(B)	Shared operation		
	(C)	Read/write operation		
	(D)	Response operation		
	(E)	Answer not known		
103.		ied Modeling Language al ritance in which a class is d		
	(A)	Single inheritance	(B)	Multiple inheritance
	(C)	Double inheritance		Class inheritance
	(E)	Answer not known		
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- 104. A simulator that functionally simulates instructions but does not provide timing information is known as
 - (A) Instruction cycle simulator
 - (B) Accurate level simulator
 - ✓(C) Instruction level simulator
 - (D) Cycle accurate simulator
 - (E) Answer not known
- 105. For the given simple C loop, consider N = 4

for
$$(i = 0 ; i \le N; i++)$$
{
$$a[i] = b[i] * c[i];$$
}

unroll the given loop twice.

- (A) for $(i = 0; i < 4; i++) \{a[i] = b[i] * c[i];\}$
- (B) for $(i = 0 ; i < 2 ; i++) \{a[i*2] = b[i*2] * c[i*2]; a[(i*2)+1] = b[(i*2)+1] * c[(i*2)+1]; \}$
 - (C) for $(i = 0 ; i<4 ; i++) \{a[i*2] = b[i*2] * c[i*2]; a[(i*2)+1] = b[(i*2)+1] * c[(i*2)+1]; \}$
 - (D) for $(i = 0; i < 2; i++) \{a[i] = b[i] * c[i];\}$
 - (E) Answer not known

106.	CPU	utilization metrics (U) of the multirate system is
/	(A)	U = CPU time for useful work/total available CPU time
	(B)	U = total available CPU time/CPU time for useful work
	(C)	U = CPU time/total system time
	(D)	U = total system time/CPU time
	(E)	Answer not known
107.	Choo	se the correct option suitable for Rate-Monotonic scheduling
	(1)	Extracts higher utilization of the CPU
	(2)	Difficult to diagnose the possibility
	(3)	Lower CPU utilization
	(4)	Easier to ensure that all deadlines will be satisfied
	(A)	(1), (2) and (4) only
V	(B)	(3) and (4) only
	(C)	(2) and (3) only
	(D)	(1) and (2) only
	(E)	Answer not known
108.	The the	data structure that holds the state of the process is known as
	(A)	Context (B) Context switching
V	(C)	Record (D) Kernel
	(E)	Answer not known

109. Choose the right option

ARM supports the following addressing modes

- (i) Register-indirect addressing
- (ii) Base-plus-offset addressing
- (iii) Auto indexing
- (iv) Post indexing
- (v) Register addressing
- (A) (i), (ii), (iii) and (iv)
- (B) (i), (ii), (iii) and (v)
- (C) (ii), (iii), (iv) and (v)
- **✓**(D) (i), (ii), (iii), (iv) and (v)
 - (E) Answer not known
- 110. Name the requirements to be satisfied by the architectural descriptions of embedded system design process
 - ✓(A) Functional and non-functional requirements
 - (B) Memory and user interface requirements
 - (C) Behavioral and non-behavioral requirements
 - (D) Accurate and in-accurate requirements
 - (E) Answer not known
- 111. Indicate the selection made in the 8051 instruction; MOV TMOD, #20H
 - (A) Timer 1, Mode 2

(B) Timer 1, Mode 1

(C) Timer 0, Mode 2

- (D) Timer 0, Mode 1
- (E) Answer not known

112.	Find the value of A and CY flags of 8051 construct	ions i	in each	of the
	following			

- (i) MOV A, # 4FH ADD B, # B1H
- (ii) MOV A, # 9CH ADD A, # 63 H

(A) (i)
$$A = FF$$
 and $CY = 1$ and (ii) $A = 00$ and $CY = 1$

(B) (i)
$$A = FF$$
 and $CY = 1$ and (ii) $A = 00$ and $CY = 0$

(C) (i)
$$A = 00$$
 and $CY = 1$ and (ii) $A = FF$ and $CY = 1$

(D) (i)
$$A = 00$$
 and $CY = 1$ and (ii) $A = FF$ and $CY = 0$

(E) Answer not known

113. Identify the invalid instruction set of 8051 microcontroller

- (A) MOV, DPTR, # 25F5 H
- (B) MOV R2, #25 H

✓(C) MOV R2, DPTR

- (D) MOV R2, 40 H
- (E) Answer not known

114. Which 8051 port need pull-up resistors to function as an I/O port?

✓(A) P0 (Port – 0)

(B) P1 (Port - 1)

(C) P2 (Port - 2)

- (D) P3 (Port -3)
- (E) Answer not known

115. How many byte instructions are the given 8051 instructions?

- (i) JN2 HERE
- (ii) LJMP
- (A) 1 byte and 2 bytes
- (B) 2 bytes and 1 byte
- (C) 2 bytes and 3 bytes
 - (D) 3 bytes and 2 bytes
 - (E) Answer not known

116. How the 8051 flag register is named?

- (A) Stack pointer
- (B) Directives
- (C) Program counter
- ✓(D) Program status word register
 - (E) Answer not known

117. What is the RAM and ROM size of 8051 microcontroller?

- (A) 128 bytes and 4 k bytes
 - (B) 4 k bytes and 128 bytes
 - (C) 256 bytes and 8 k bytes
 - (D) 8 k bytes and 256 bytes
 - (E) Answer not known

118.	How	many conditional flags are there in 8086 microprocessor?		
~	(A)	6	(B) 7	
	(C)	8	(D) 10	
	(E)	Answer not known		
119.	How set?	many RST (Restart) instruction	ons are there in 8085 instruction	
~	(A)	8	(B) 4	
	(C)	2	(D) 6	
	(E)	Answer not known		
120.	Two	16-bit registers to hold memoral	ory address of 8085 architecture	
		BC register pair		
	(ii)	Accumulator		
	(iii) S	Stack Pointer		
	(iv) 1	Program counter		
	(A)	(i) and (ii)		
	(B)	(ii) and (iii)		
	(C)	(ii) and (iv)		
~	(D)	(iii) and (iv)		
	(E)	Answer not known		

121.	21. A set of process indicators that lead to long term software process improvements are known as						
	(A)	Project metrics	(B) Process metrics				
	(C)	Product metrics	(D) Program metrics				
	(E)	Answer not known					
122.	"CM	IM" stands for					
	(A)	Computer Maintenance Ma	anagement				
	(B)	Company Maintenance Ma	nagement				
V	(C) Capability Maturity Model						
	(D) Capability Management Model						
	(E)	(E) Answer not known					
123.		ch one executes a system in bnormal quantity, frequency	a manner that demands, resources y or volume?				
	(A)	Recovery testing	(B) Security testing				
	(C)	Stress testing	(D) Performance testing				
	(E)	Answer not known					
124.		focuses on the int	ternal processing logic and data s of a component"				
	(A)	Integration test	(B) Validation test				
	(C)	Verification test	✓(D) Unit test				
	(E)	Answer not known					

~	(A)	White box testing							
	(B)	Black box testing							
	(C)) Basis path testing							
	(D)	Glass box testing and basis pa	ath t	esting					
	(E)	Answer not known							
126.		process of executing a program ng errors are known as	m (o	r) system with the intent of					
	(A)	software design	(B)	software coding					
~	(C)	software testing	(D)	software maintenance					
	(E)	Answer not known							
127.	Whic	ch one of the following is the be	st k	nown technique using LOC?					
	(A)	CMM	(B)	RMM					
V	(C)	COCOMO	(D)	SCM					
	(E)	Answer not known							
128.	Earn	ned value analysis is a measure	of						
13.0	(A)	Project	(B)	Progress					
	(C)	Cost	(D)	Performance					
	(E)	Answer not known							

125. The internal view of software testing is termed as

129.		is a collection of soft ucts and quality assurance f		engineering milestones, work
/	(A)	Task set	(B)	Task target
		Task goal		Task analysis
		Answer not known		
130.	Whic		ents o	f linear and parallel process
	(A)	Waterfall model	(B)	Incremental process model
	(C)	Spiral model	(D)	Prototype model
	(E)	Answer not known		
131.	In ag	gile process "XP" stand for		
	(A)	Extreme process	(B)	Extreme programming
	(C)	Extra process	(D)	Extra program
	(E)	Answer not known		
132.	In so	ftware process, "40-20-40 ru	le" is a	pplicable for
~	(A)	Distribution of effort	(B)	Distribution of work place
	(C)	Distribution of materials	(D)	Distribution of project
	(E)	Answer not known		
133.	Chec	k list and brain storming are	e the a	pproaches of
~	(A)	Risk identification	(B)	Risk mitigation
	(C)	Risk planning	(D)	Risk monitoring
	(E)	Answer not known		
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134.	entir	is a transformation to e system.	hat i	mposed on the design of an
	(A)	System style	(B)	Data style
V	(C)	Architectural style	(D)	Construction style
	(E)	Answer not known		
135.		lem of scope, problem of cility are related to	unde	rstanding and problem of
	(A)	Inception	(B)	Elicitation
	(C)	Elaboration	(D)	Negotiation
	(E)	Answer not known		
136.	SSA	stands for		
	(A)	System Software Analysis		
	(B)	Software System Analysis		
~	(C)	Structured System Analysis		
	(D)	Structure Standard Analysis		
	(E)	Answer not known		
137.	Whic	th one is the primary requirem	ents	validation mechanism?
~	(A)	Technical review	(B)	Technical interview
	(C)	Technical inspection	(D)	Technical design
	(E)	Answer not known		

	(A)	System Requirements Specification					
V	(B)	Software Requirements Spec	ificat	tion			
	(C)	Software Reengineering Specification					
	(D)	System Reengineering Specif	icati	on			
	(E)	Answer not known					
139.		ules be "characterized by desig r" is known as	gn de	cisions that prevents from all			
	(A)	Encapsulation	(B)	Information Hiding			
	(C)	Mophing	(D)	Modularity			
	(E)	Answer not known					
140.		overall structure of the softweeptual integrity for a system is					
	(A)	Software Process	(B)	Software Design			
~	(C)	Software Architecture	(D)	Software Algorithm			
	(E)	Answer not known					
141.	"CH	ECK" clauses can be used	d to	check each			
	indiv	ridually whenever inserted or r	nodi	fied.			
V	(A)	row	(B)	column			
	(C)	table	(D)	all the above			
	(E)	Answer not known					

138. SRS stands for

142. Loan Relations

Borrower Relations

Loan-No	Branch-	Amount	Customer-	Loan-no	
	Name		name		
L-170	Downtown	3000	Jones	L-170	
L-230	Redwood	4000	Smith	L-230	
L-260	Perryridge	1700	Hayes	L-155	

Write the Output:

Loan left outer join borrower on loan.loan number = borrower.loan – number

as lb (loan -number)

- (A) L-170, L-230
- (B) L-170, L-230, L-260
 - (C) L-170, L-230, L-260, L-155
 - (D) L-170, L-230, L-155
 - (E) Answer not known
- 143. Which SQL allow the programmers to create, execute and prepare for subsequent use?
 - (A) Embedded SQL
 - (B) Dynamic SQL
 - (C) Static SQL
 - (D) PL/SQL
 - (E) Answer not known

144.		ch data structure that correspora expression?	pond	ls to an extended relationa
	(A)	List	(B)	Tree
~	(C)	Query tree	(D)	Binary Tree
	(E)	Answer not known		
145.	Some	etimes Project-Join Normal for	m (I	PJ NF) otherwise called as
	(A)	First Normal form	(B)	Third Normal form
	(C)	Fourth Normal form	(D)	Fifth Normal form
	(E)	Answer not known		
146.	funct the	lation schema R is in —————————————————————————————————	unct	tional dependencies in $F^{\scriptscriptstyle +}$ o
	*	$\alpha - \beta$ is a trivial functional de	epen	dency (that is, $\beta \subseteq \alpha$)
	*	α is a super key for schema I	?	
	(A)	First Normal Form		
	(B)	Second Normal Form		
	(C)	Third Normal Form		
~	(D)	Boyce - Codd Normal Form		
	(E)	Answer not known		

147.	-	is any condition that the database must always						
	satis	sfy						
	(A)	Conditional statements (B) Check constraint						
(C) Assertions (D) Authorization								
	(E)	Answer not known						
148.	Data	abase can be modified by						
	(A)	Insertion, rename, deletion						
	(B)	Insertion, deletion, selection						
	(C)	Selection, deletion, update						
V	(D)	Insertion, deletion, update						
	(E)	Answer not known						
149.	An I	Entity set that has a primary key is termed						
	(A)	Specialization Entity (B) Aggregation Entity						
~	(C)	Strong Entity (D) Weak Entity						
,	(E)	Answer not known						
150.	Α —	of a relation is a set of one or more attributes						
		se values are guaranteed to identify tuples in the relation						
	uniq	juely.						
	(A)	Candidate key (B) Super key						
	(C)	Instance (D) Foreign key						
	(E)	Answer not known						
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151.	Whic	ch of the following is not an advantage of DBMS?
	(A)	Increased data consistency
	(B)	Reduced data redundancy
	(C)	Improved data security
~	(D)	Increased data isolation
	(E)	Answer not known
152.	The	two models preceded the relational data model was
	(A)	Entity Relationship model, Network data model
V	(B)	Network data model and hierarchical data model
	(C)	Hierarchical model and Entity relationship model
	(D)	Semistructured model and Network model
	(E)	Answer not known
153.	The relat	allows to combine information from any two
	(A)	Domain constraint (B) Primary keys

(D) Cartesian product

Unique keys

(C)

154.	Mat	tch the	follo	wing R	AID I	Levels
	(a)	RAID	-2		1.	Block - interleaved party
	(b)	RAID	-3		2.	Block - interleaved distributed party
	(c)	RAID	-4		3.	Error correcting codes
	(d)	RAID	5		4.	Bit – interleaved party
		(a)	(b)	(c)	(d)	
	(A)	4	3	1	2	
~	(B)	3	4	1	2	
	(C)	3	4	2	1	
	(D)	1	2	3	4	
	(E)	Ansv	ver no	t know	'n	
155.	Tho	mas' v	vrite 1	rule is	applie	ed for ——protocol.
~	(A)	Time	estam	p – Or	derin	g Protocol
	(B)	Cone	currer	ncy con	trol p	protocol
	(C)	Vali	datior	n – Bas	ed pr	otocol
	(D)	Opti	mistic	c concu	rrenc	ey control protocol
	(E)			ot knov		
156.	and		ataba	se has		ter the transaction has been rolled back restored to its state prior to the start of
	(A)	Acti	ve			(B) Partially committed
~	(C)	Abo	rted			(D) Committed
	(E)	Ans	wer no	ot knov	vn	

157.	Whi	ch datastructure is used for	dynamic hashing technique?	
	(A)	Stack	(B) Queue	
	(C)	Linked list	(D) Binary Tree	
	(E)	Answer not known		
158.	by s		a new record that is being inserted a condition that a set of record T1 must satisfy?	
_	(A)	Deletion problem	(B) Insertion problem	
	(C)	Phantom problem	(D) Update problem	
	(E)	Answer not known		
159.	subn Find	nit the transactions T_1 and	mple, two airline reservation agent T_2 at approximately the same time the terms of the same of the	e.
	(A)	Many outcomes	(B) 2 outcomes	
	(C)	3 outcomes	(D) 1 outcome	

Answer not known

(E)

- 160. Which failure refer to a list of problems that includes power or air conditioning failure, fire, theft, sabotage, overwriting disks or tapes by mistake and mounting of a wrong tape by the operator?
 - (A) System error
 - (B) Disk failure
 - (C) Catastrophes
 - (D) Concurrency control enforcement
 - (E) Answer not known
- 161. Weight and profit value of each item is as given below and w = 20

$$pi = \{30, 21, 18\}$$

$$wi = \{18, 15, 10\}$$

$$i = \{1, 2, 3\}$$

Compute the maximum optional profits using greedy method

- (A) $\sum wi \ xi = 16.5$ and $\sum xi \ pi = 26.5$
- (B) $\sum wi \ xi = 20 \ \text{and} \ \sum xi \ pi = 32 \cdot 8$
 - (C) $\sum wi \, xi = 20$ and $\sum xi \, pi = 32$
 - (D) $\sum wi \ xi = 20 \ \text{and} \ \sum xi \ pi = 30$
 - (E) Answer not known

162. Complexity Analysis of Hamiltonian problem is

(A)
$$\frac{(n-1)^n - 1}{n-2}$$

(B) $\frac{(n-1)^{n+1}}{2n}$

$$(B) \quad \frac{(n-1)^{n+1}}{2n}$$

$$(C) \quad \frac{(n-1)^{2n}}{2n}$$

(D)
$$\frac{((n-1)(n-1)-1)}{2n^2-1}$$

(E)Answer not known

163. The time complexity of Strassen's matrix multiplication using divide and conquer method is

(A)
$$T = \theta \left(n^{\log_2 7} \right)$$

(B)
$$T = \theta \left(n^{2.81}\right)$$

(D)
$$T = \theta \left(n^2 \log n \right)$$

(E) Answer not known

164. Choose the correct method which is to keep a list of all elements that has to the same value using standard library implementations

- (A) Separate chaining
- (B) Hash with linked list

(C) Linear probing

- (D) Hash-sha-256 algorithm
- (E)Answer not known

- 165. Choose which is not correct about Amortized Analysis
 - (A) Finding average running time per operation over worst case
 - (B) Assumes worst case input and does not allow random choice
 - ✓(C) Time can be computers within one execution of algorithm
 - (D) Not similar to average case analysis
 - (E) Answer not known
- 166. Using following Weighted Adjacency matrix and choose the all pair weight adjacency matrix

$$D^{(0)} = \begin{array}{ccc} 1 & 2 & 3 \\ 1 & 0 & 8 & 5 \\ 2 & 0 & \infty \\ 3 & \infty & 1 & 0 \end{array}$$

$$\begin{array}{c} \checkmark \text{(A)} & \begin{bmatrix} 0 & 6 & 5 \\ 2 & 0 & 7 \\ 3 & 1 & 0 \end{bmatrix}$$

(B)
$$\begin{bmatrix} 0 & 8 & 5 \\ 2 & 0 & 7 \\ 3 & 1 & 0 \end{bmatrix}$$

(C)
$$\begin{bmatrix} 0 & 8 & 5 \\ 2 & 0 & 7 \\ \infty & 1 & 0 \end{bmatrix}$$

(D)
$$\begin{bmatrix} 0 & 8 & 5 \\ 0 & 0 & \infty \\ 3 & 1 & \infty \end{bmatrix}$$

(E) Answer not known

	he :	function which is used to de	letin	ig an element from heap is
1	A)	Percolate down	(B)	Percolate up
((C)	Heapifying	(D)	Minmax heap
(]	E)	Answer not known		
re		ee which describes the sorti esent a key comparison and th		
(4	A)	Binary tree	(B)	Binary search tree
(0	C)	AVL tree	(D)	Decision tree
(]	E)	Answer not known		
		rting technique that does not ory is called	use	any intermediate auxiliary
1	(A)	In place or out place sort		
(1	B)	Internal or external sort		
(C)	Comparison Vs distribution		
()	D)	Computation Vs distribution		
(E)	Answer not known		
170. I	n wł	nich applications trees are risk	use	d?
(.	A)	Set representation	(B)	Decision trees
(C)	Game trees	(D)	Memory management
(E)	Answer not known		

171.		can obtain the time completed ods, like master theorem and s	222	
	(A)	Quick sort	(B)	Bubble sort
~	(C)	Merge sort	(D)	Radix sort
	(E)	Answer not known		
172.		sorting technique that is oach is	ba	sed on divide-and-conquer
	(A)	Selection sort	(B)	Insertion sort
	(C)	Quick sort	(D)	Merge sort
	(E)	Answer not known		
173.		hich tree the height of the let sub tree differ by not more tha		
	(A)	Red black tree	(B)	Splay tree
V	(C)	AVL tree	(D)	Expression tree
	(E)	Answer not known		
174.	addr	imes 12 matrix is implemented u ess of array is 200 and the vess of the element A [4, 7] in re	vord	size is 2 then compute the
	Assu	me that the lower bound of bot	h ro	w and column indices is 1.
~	(A)	284	(B)	326
	(C)	282	(D)	280
	(E)	Answer not known		

1	75.	Check	the	valid	operations	on iterators:
-		CILCUIL		1 CLAACE	OPPORTURA	OTT TOOT OFFICE

- (i) itr++ and ++itr \rightarrow advances the iterator itr to the next location.
- (ii) *itr → returns a reference to the object store at iterator.
- (iii) itr1! = itr2 → returns true if iterators itr1 and itr2 refer to a different location.
- (A) (i) and (ii)

(B) (i) and (iii)

(C) (ii) and (iii)

- (D) All the above
- (E) Answer not known
- 176. Name the date structure for the following snipped code or procedure struct node

int i;

{

struct node * NEXT;

struct node * previous;

};

typedef struct node NODE;

- (A) Doubly linked list-node
 - (B) Single linked list-node
 - (C) Circular linked list-node
 - (D) Circular queue linked list-node
 - (E) Answer not known

177.	Which	node	pointers	should	be	upda	ated	if a	a new	node	"B"	is to	be
	inserte	ed in t	he middle	e of "A'	an	d "C"	nod	es	of sing	gly lin	ked	list?	

- (i) NEXT pointer of A and NEXT pointer of C
- (ii) NEXT pointer of B and NEXT pointer of C
- (iii) NEXT pointer of B
- (iv) NEXT pointer of A and NEXT pointer of B
- (A) (i) and (ii)

(B) (ii)

(C) (iii) and (i)

(D) (iv)

(E) Answer not known

178. Identify the correct statement about circular linked list.

- (A) It allows complete list traversal starting from any of the node
 - (B) It allows complete list traversal only if we begin from the first node
 - (C) Next part of the last node is null
 - (D) Next part of first node is pointing to null
 - (E) Answer not known

179. Choose the type of double-ended queue.

- (A) Input restricted dequeu
- (B) Output restricted deque
- (C) Linked list implementation of double ended queue
- (D) (A) and (B)
 - (E) Answer not known

180.	If 2,	1, 5, 8	3 are the	e stack	conte	nts	with el	ement 2 be	eing at	the top of
	the	stack	, then	what	will	be	stack	contents	after	following
	oper	ations	3.							
		Push	ı (11).							

Pop()

Pop()

Pop()

Push (7)

- 11, 2, 1 (A)
 - (B) 8, 11, 7
- (C) 7, 5, 8
 - (D) 5, 8, 7
- (E) Answer not known

181. Match it

List of objects in Purpose JavaScript

- (a) Math object
- To obtain true or false values 1.
- (b) Number object
- 2. To obtaining date and time
- (c) Date object
- Numberic properties 3.
- (d) Boolean object
- Mathematical computations 4.
- (a) (b) (c) (d) 1
- (B) 2 -3 -4 1
- (C) 3 -2 1
- (D) 1 3
- (E) Answer not known

182.	-	separates the content ar	rea from the box's border in CSS.	
	(A)	Margin	(B) Border	
~	(C)	Padding	(D) Edge	
	(E)	Answer not known		
183.	Choc	<u>all</u> ;	for the given entity reference	
	(A)	© \	(B) ∀	
	(C)	α	(B) ∀ (D) &	
	(E)	Answer not known		
184.	Ever	nt is an that represen	ents a change in the environment	•
	(A)	Handler	(B) Activity	
	(C)	Registration	(D) Internet program	
	(E)	Answer not known		
185.	A Ca	nvas element is a		
~	(A)	White rectangle	(B) Black rectangle	
	(C)	Red rectangle	(D) Green rectangle	
	(E)	Answer not known		

186. Dom makes it possible fo	186.	Dom	makes	it	possible	for
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- (A) Establishing a browser history
- (B) Javascript code to communicate with the browser
 - (C) Inspect the elements
 - (D) Establishing a communication between panel and web pages
 - (E) Answer not known

187. Output of the following assignment operators / expressions

let
$$x = 2$$
$$x+=2$$
$$x-=2$$
$$x^*=6$$
$$x/=3$$
$$x^**=2$$

- (A) 6
- (C) 17
- (C) 17

- (B) 16
 - (D) 12
- (E) Answer not known
- 188. Which of the following represents legal flow control statements in java?

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(A) break;

(B) break();

(C) continue (inner);

- (D) exit();
- (E) Answer not known

189.	Wha	at decides thread priorit	ty?	
	(A)	Process	(B)	Thread
~	(C)	Thread scheduler	(D)	Process scheduler
	(E)	Answer not known		
190.	Give	en the code		
	strir	ng s = new string ("abc"));	
	Whi	ch of the following calls	are valid?	
	(A)	s.toUppercase()	(B)	s.replace('a', 'A')
	(C)	s.strim()	(D)	both (A) and (B)
	(E)	Answer not known		
191.	Wha	at will be the result of the	he expression	n 13 and 25?
	(A)	38	(B)	25
~	(C)	9	(D)	12
	(E)	Answer not known		
192.	The	jdbc API classes are su	pported by the	he java package
	(A)	java.servlet.*	(B)	java.awt.*
	(C)	java.io.*	(D)	java.sql.*
	(E)	Answer not known		

193.	Whi	ch method is used to create a new instance 8 a server in node.js?
~	(A)	http.create instance()
	(B)	http. newServer()
	(C)	http. createServer()
	(D)	http. createServer Instance()
	(E)	Answer not known
194.	Ider	ntify the correct definition of a package
	(A)	A package is a collection of editing tools
	(B)	A package is a collection of classes
V	(C)	A package is a collection of classes and interfaces
	(D)	A package is a collection of interfaces
	(E)	Answer not known

- ✓(A) 65,515 bytes of data
- (B) 4 gigabytes of data
- (C) 65,500 bytes of data
- (D) 65,536 bytes of data
- (E) Answer not known

196	. Wh	ich constructor of datagram socket is used to create a datagram ket and binds it with the port number?
	(A)	Datagramsocket()
~	(B)	Datagramsocket (int port.net Address address)
	(C)	Datagramsocket (int port)
	(D)	Datagramsocket (inetAddress address)
	(E)	Answer not known
197.	The abou	interface enables a servlet to obtain information at a client request.
V	(A)	HttpservletRequest (B) HttpservletResponse
	(C)	HttpRequest (D) HttpResponse
	(E)	Answer not known
198.	Whi	ch module in Node.js provides the EventEmitter class?
	(A)	fs (B) http
	(C)	util (D) events
	(E)	Answer not known
199.	The	main purpose of Node.js event loop
		Processing synchronous tasks in order
	(B)	Manage multiple threads of execution
~	(C)	Handling asynchronous tasks without blocking the I/O operations
	(D)	Both (A) and (B)
	(E)	Answer not known
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200. The	e interface enablermation that is associated	les a servlet to read and write the state I with an HTTP session.
(A)	HttpRequest	(B) HttpSession Response
(C)	HttpSession	(D) HttpSession Request
(E)	Answer not known	